

## REQUEST FOR

## CONTINUED EXAMINATION (RCE) **TRANSMITTAL**

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995. See The American Inventors Protection Act of 1999 (AIPA).

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Application Number	09/945500
Filing Date	August 30, 2001
First Named Inventor	Leonard Forbes
Group Art Unit	2818
Examiner Name	Ly D. Pham
Attorney Docket Number	1303.029US1
Customer No.	21186

This is a Request for Continued Examination (RCE) under 37 CFR § 1.114 of the above-identified application entitled PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS.

Submission required under 37 C.F.R. § 1.114

- Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on . 1.
- 2. \_\_\_ Consider the arguments in the Appeal Brief or Reply Brief previously filed on .
- 3. An Amendment Under 37 CFR § 1.116 (pages) is enclosed.
- A new power of attorney ( pages ) is enclosed. 4.
- 5. X An Information Disclosure Statement is enclosed (2 pages).
  - Form(s) 1449 a. 2
  - Copies of IDS Citations b. 38
- A check in the amount of \$770.00 is attached to pay the RCE filing fee required under C.F.R. § 1.17(e). 6. X
- 7. X The Commissioner is hereby authorized to credit overpayments or charge any fees set forth in 37 CFR §§ 1.16 through 1.18 to Deposit Account No. 19-0743.
- A Petition for Extension of Time in the prior application ( pages ) is enclosed along with a check in the 8. \_\_ amount of to pay the extension fee.

9. <u>X</u> Others: Communication Concerning Related Applications (2 pgs.)

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

No. 40.957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Attn-Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 11th day of March, 2004.

Name

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**PATENT** S/N 09/945

## ED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard Forbes

Examiner: Ly D. Pham

Serial No.:

09/945500

Group Art Unit: 2818

Filed:

August 30, 2001

Docket: 1303.029US1

Title:

PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH

LOW TUNNEL BARRIER INTERPOLY INSULATORS

## COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u> 09/945507	Filing Date August 30, 2001	Attorney Docket 1303.014US1	Title FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
09/945395	August 30, 2001	1303.019US1	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
09/945512	August 30, 2001	1303.027US1	IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
10/081818	February 20, 2002	1303.045US1	ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPLOY INSULATORS
10/177096	June 21, 2002	1303.063US1	GRADED COMPOSITION METAL OXIDE TUNNEL BARRIER INTERPOLY INSULATORS

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Serial Number: 09/945500

Filing Date: August 30, 2001

Title: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

10/783695	February 20, 2004	1303.019US2	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
Unknown	February 18, 2004	1303.063US2	GRADED COMPOSITION METAL OXIDE TUNNEL BARRIER INTERPOLY INSULATORS
10/788810	February 27, 2004	1303.027US2	IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

Respectfully submitted,

LEONARD FORBES

By Applicant's Representatives,

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<u>CERTIFICATE UNDER 37 CFR 1.8:</u> The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this <u>11th</u> day of <u>March</u>, 2004.

Name

Signature